
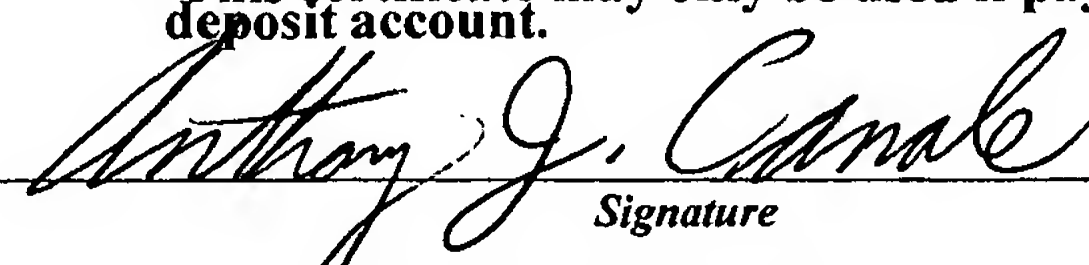


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))		Docket No. BUR920030119US1	
In Re Application Of: Steven H. Voldman			
Serial No. 10/707,283	Filing Date 12/03/2003	Examiner UNASSIGNED	Group Art Unit UNASSIGNED
Title: Apparatus and Method for electronic Fuse with Improved ESD Tolerance			
<p>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p>37 CFR 1.97(c)</p> <p>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p>OR</p> <p><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p>			

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. BUR920030119US1
In Re Application: Steven H. Goldman			
Serial No. 10/707,283	Filing Date 12/03/2003	Examiner UNASSIGNED	Group Art Unit UNASSIGNED
Apparatus and Method for electronic Fuse with Improved ESD Tolerance			
Payment of Fee (Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))			
<input type="checkbox"/> A check in the amount of _____ is attached.			
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. 09-0456 as described below.			
<input type="checkbox"/> Charge the amount of _____			
<input checked="" type="checkbox"/> Credit any overpayment.			
<input checked="" type="checkbox"/> Charge any additional fee required.			
Certificate of Transmission by Facsimile*		Certificate of Mailing by First Class Mail	
<div>I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (F:</div> <div>_____ (Date)</div> <div>_____ Signature</div> <div>_____ Typed or Printed Name of Person Signing Certificate</div>		<div>I certify that this document and fee is being deposited on <u>03/08/04</u> with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div> <div> Signature of Person Mailing Correspondence</div> <div><u>C. M. Weller</u> Typed or Printed Name of Person Mailing Certificate</div>	
<div><div>*This certificate may only be used if paying by deposit account.</div><div> Signature</div><div>Anthony J. Canale, Reg. No. 51.526 IP Law Department, 972E IBM Corporation 1000 River Street Essex Junction, VT 05452 Telephone: 802-769-8782</div></div> <div style="text-align: right; margin-top: 20px;">Dated: <u>02/27/2004</u></div>			
CC:			



BUR920030119US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of:
Steven H. Voldman

Group Art Unit: Unknown

Serial No.: Not assigned yet ^{10/}707, 283

Examiner: unknown

Filed: Concurrently herewith 12/03/03

For: **APPARATUS AND METHOD FOR ELECTRONIC FUSE WITH IMPROVED
ESD TOLERANCE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.56**

Sir:

Under provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicants respectfully bring the document listed on the attached Form PTO-1449 to the attention of the Examiner in charge of the above-identified application. A copy of the document cited is enclosed for the convenience of the Examiner.

This citation does not constitute an admission that the cited reference is relevant or material to the claims nor should it be construed as a representation that no other art than that identified exists. It is merely cited as constituting related art of which the applicant is aware.

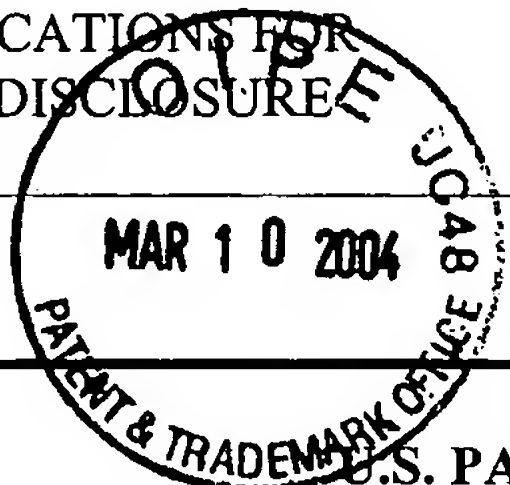
It is respectfully requested that this document be considered by the Examiner and formally made of record in this application.

Respectfully submitted,

Andrew M. Calderon
Reg. No. 38,093

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(703)712-5000

SUPPLEMENTAL FORM PTO-1449 (Modified)		ATTY. DOCKET NO. BUR920030119US1	SERIAL NO. Unassigned <i>10/702,283</i>
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT: Steven H. Voldman	
(Use several sheets if necessary)		FILING DATE: <i>12/03/03</i> Concurrently Herewith	GROUP: Unassigned



REFERENCE DESIGNATION

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

		W. J. Craig, et al., "On-Chip Electrically Programmable Fuse", IBM Technical Disclosure Bulletin, Vol. 29, No. 3, August 1986.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.